



Everspin Announces New STT-MRAM EMxxLX xSPI Family of Products

Chandler, AZ May 3, 2021 – Everspin Technologies, Inc. (NASDAQ:MRAM), the world’s leading developer and manufacturer of Magnetoresistive Random Access Memory (MRAM) persistent memory solutions, has announced that the company has begun customer sampling of its new family of SPI/QSPI/xSPI interface MRAM products. The EMxxLX is the world’s highest performance persistent memory with full read and write bandwidth of 400 Megabytes per second through the new JEDEC expanded Serial Peripheral Interface (xSPI) standard interface. With densities ranging from 8Mbit up to 64Mbit, this family of products is targeted for use in industrial IoT and embedded systems applications.

“The EMxxLX family provides a breakthrough level of performance along with an ease-of-use approach. This is achieved by conforming to a broad range of SPI/QSPI/xSPI industry standards, and by bringing extremely high bandwidth, low latency, non-volatile writing capability. These features will both enhance and simplify system design for use with practically all microcontroller, microprocessor, and FPGA platforms already in the market.”, said Sanjeev Aggarwal, President and CEO of Everspin Technologies. The family is offered in two package types, a 24-ball BGA and 8-pin DFN, both industry standard packages. Everspin has optimized its proprietary Spin-transfer Torque MRAM (STT-MRAM) to bring 10 year data retention, virtually unlimited write cycle endurance and a 200MHz DDR clock rate over a full industrial temperature range of minus 40C to plus 85C.

Customers are evaluating samples of the product now. “With Everspin’s announcement of their latest EMxxLX product family, and the increase in performance and densities, Siemens is eager to explore the advantages for our next generation platform evaluation.”, said Wolfgang Ernst, Senior Key Expert, Siemens.

The new JEDEC xSPI standard is expected to become the most prevalent way to access data memory and code storage in embedded systems. The EMxxLX will be supported by the xSPI Multiple Bus Memory Controller from Everspin’s partner, Synaptic Laboratories Ltd (SLL). SLL’s xSPI MBMC IP supports most popular FPGA device families, and soon ASIC, at up to the full 400 Mbytes/s wire speed.

“The EMxxLX family changes the non-volatile memory game for all embedded designs. When combined with SLL’s xSPI Multiple Bus Memory Controller, and COTS development boards, embedded system designers will be able to rapidly take advantage of the exciting benefits of these memories.”, said Benjamin Gittins, CTO of Synaptic Laboratories Ltd.

Volume production shipments of the EMxxLX series will be available in the second half of 2022.

About Everspin Technologies Everspin Technologies, Inc. is the world’s leading provider of Magnetoresistive RAM (MRAM). Everspin MRAM delivers the industry’s most robust, highest performance non-volatile memory for Industrial IoT, Data Center, Automotive, and other mission-critical applications where data persistence is paramount. Headquartered in Chandler, Arizona, Everspin



provides commercially available MRAM solutions to a large and diverse customer base. For more information, visit www.everspin.com. NASDAQ: MRAM.

Company Contact:

Joe O'Hare

T: 512-975-6669

E: joe.ohare@everspin.com

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