



Magnetic Device Test Engineer

Everspin Technologies is searching for exceptional candidates to fill an Engineer position in Advanced MRAM development.

Scope of Responsibility/Expectations

The Memory Device Test Engineer will be competent in Memory Test Development such as implementing Memory Test Plans, testing to data sheet specs and debugging new products. In addition, this Engineer will be required to develop unique and specialized characterization routines in coordination with the Design and R&D teams to further magnetic technology development. This position requires a general understanding of how MRAM and DRAM work, and the successful candidate will have the ability to drive projects independently.

Specific Knowledge

- Will be experienced in production and/or new product memory test environments, methodologies, and test requirements
- Will possess the ability to execute new product introduction test plans to satisfy design performance on various types of MRAM related memory products.
- Will implement novel characterization techniques and test methodologies for product/device performance debug and improvements.
- Will possess good team work and communication skills to work with R & D, Design and Manufacturing to accomplish the various test programming engineering tasks.
- Test development experience on emerging memory technologies is desirable.
- Basic Magnetic Memory Functionality.
- Characterization and Analysis Techniques for Magneto electronic Memory Devices is desirable.

Desired skills

8 years ATE programming on at least one type of test platform, Advantest, 83/93000 (HP) or Teradyne, with preference to the 83/93000 (HP).

A strong knowledge on scripting and programming techniques.

A strong knowledge on memory (DRAM /MRAM/ Flash) test methodologies, including DFTs, DFM, and MBIST.

Experienced in high speed DDR Memory Interface.

3/4 characterization development is a plus.

Experience in developing product and characterization test routines, for detailed device performance analysis.

Ability to drive a project independently.

Ability to successfully interface with various organizations, including Design, R&D and Manufacturing.

An understanding of the relationship between magnetic and magneto resistive properties of magnetic material and processing to magneto electronic memory device performance.

Experience in new design validations and new products introductions.

Strong understanding of software languages HPVee and/or Visual Basic.
Basic semiconductor device knowledge in CMOS.
Magnetic Memories previous work experience (Test, Process, Design) is a plus.

Master's degree in Engineering, Computer Engineering or a related equivalent discipline required. A minimum of 8 years of experience in Memories or Complex IC Testing methodology.

The Company

Everspin Technologies is the world's first volume MRAM supplier and continues to lead the industry by advancing the technology and expanding its MRAM product portfolio. The open position is at the company headquarters located in Chandler, Arizona (greater Phoenix metropolitan area). It is a salaried position that includes benefits including: medical, dental, vision plan, paid time off, and a 401k retirement plan.

Contact

Please send resume to jobs@everspin.com