

ATE Test Development Engineer – Chandler, AZ

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Responsibilities

- Work with Design and Device engineering to establish test methodologies and architectures for new technology introduction.
- Develop the Test Hardware and Software for ATE platforms.
- Hands on development and coding of initial ATE test program for new products.
- Develop characterization program, debug and characterize during new device or product introductions.
- Support RMA electrical validation.
- Support and lead off-shore test development team.

Requirements

The ideal candidate will have experience on both the Advantest 55XX memory testers and the Advantest/Verigy 93000 family.

- ATE Test development experience on Memory Test Platforms – Advantest 55xx(DDR2/DDR3 Products) and Verigy 93K
- Experience on HSM (High Speed Memory or MTP (Memory Test Software Plus)
- Minimum of 3 years of Memory Test Development - DRAM or SRAM development experience, including MBIST, high speed DDR interface and DFT methodologies.
- Experience with high speed interfaces like DDR3/DDR4 testing is a plus. Experience with other ATE Platforms: Verigy, J750 and Advantest.
- Experience with both Wafer Probe and Package Test

Everspin Technologies

Headquartered in Chandler, Arizona, Everspin Technologies, Inc. is the worldwide leader in designing, manufacturing, and commercially shipping discrete and embedded Magnetoresistive RAM (MRAM) and Spin-Torque MRAM (ST-MRAM) into markets and applications where data persistence and integrity, low latency, and security are paramount. With over 50 Million MRAM and ST-MRAM products deployed in data center, cloud storage, energy, industrial, automotive, and transportation markets, Everspin has built the strongest and fastest growing foundation of MRAM users in the world.

This opportunity is at the company headquarters located in Chandler, Arizona (greater Phoenix metropolitan area). It is a salaried position with benefits including: medical, dental, vision plan, paid time off, and a 401k retirement plan.