



Memory Designer for DDR Interface and Control Block Circuitry 48769

Everspin Technologies is searching for exceptional candidates to fill a Member of the Technical Staff position in MRAM circuit design.

Scope of Responsibility/Expectations

Work closely with other members of a small, focused, team with responsibility for designing both the pads and the control circuitry associated with a DDR interface. Design at the transistor level circuitry in the control block sitting between the DDR pads and the standard memory signals going to and from the array. This control block decodes the instructions from the bus, lines up input and output signals with the high frequency clock using DLLs or PLLs, and converts input signals to array commands and takes array output signals, such as, data, and sends them to the pads following the DDR standard protocol. Work under the supervision of a senior project leader. Design, simulate, and supervise layout of blocks of circuitry. Work with product and test engineers evaluating and debugging circuit performance after silicon processing.

Specific Required Knowledge

- Circuit theory
- PLL/DLL design techniques
- Probability and statistics
- Logic design
- Impact of layout parasitic capacitance and resistance on signal integrity, speed, and power dissipation, along with layout techniques to minimize these effects

Useful Skills

- Transistor level simulation
- Parasitic extraction
- Critical path modeling
- Physical design, i. e., layout
- Logic verification
- Mixed-mode Verilog

Education and Experience

Masters or equivalent in Electrical Engineering, Physics, Computer Science, or related fields. A track record of placing memory chips into production is required. Experience designing DDR pads that meet JEDEC electrical specifications and that have industry standard ESD tolerance is desired. Experience with DRAMs or with DRAM Controller s and PHYs is applicable. Demonstration of innovation and leading-edge expertise through publications and patents is desired.

The Company

Everspin Technologies is the world's first volume MRAM supplier and continues to lead the industry by advancing the technology and expanding its MRAM product portfolio. The open position is at the company design center located in Austin, Texas. It is a salaried position that includes benefits including: medical, dental, vision plan, paid time off, and a 401k retirement plan.

Contact

Please send resume to jobs@everspin.com