

# MR25H256 – 256Kb SPI MRAM

## VERILOG Model Readme File

### Introduction

This is the VERILOG model for the MR25H256 – a 256Kb SPI MRAM Product from Everspin.

### Device Summary

The **MR25H256** is a 262,144-bit magnetoresistive random access memory (MRAM) device organized as 32,768 words of 8 bits. The **MR25H256** offers serial EEPROM and serial Flash compatible read/write timing with no write delays and unlimited read/write endurance. Unlike other serial memories, both reads and writes can occur randomly in memory with no delay between writes. The **MR25H256** is available in a small footprint 5 mm x 6 mm 8-pin DFN package that is compatible with serial EEPROM, Flash, and FeRAM products. Data Retention of greater than 20 years is guaranteed.

### Model Release Notes

Product Datasheet: [http://www.everspin.com/PDF/EST\\_MR25H256\\_prod.pdf](http://www.everspin.com/PDF/EST_MR25H256_prod.pdf)

Model Revision: 1.0

Model Release Data: September 2010

### Files

- |                      |                      |
|----------------------|----------------------|
| 1. Readme_MR25H256   | - This File          |
| 2. MR25H256.V        | - Device Model       |
| 3. Config_MR25H256.V | - Configuration File |

### VERILOG Model

MR25H256.V is the abstracted model of the 256Kb SPI MRAM.

**Warning:** These VERILOG models are provided “as is” without warranty of any kind, including, but not limited to, any implied warranty of merchantability and fitness for a particular purpose.

## Revision History

Date	Revision	Changes
9/26/2010	1.0	New Model – Initial Release

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