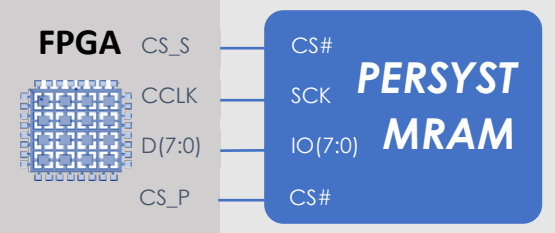
PERSYST MRAM – FPGA Support with EMxxLX

The EMxxLX family of ***PERSYST*** MRAM devices offers high performance, multiple I/O, SPI compatible MRAM devices featuring a low pin count SPI bus interface with supported frequencies up to 200 MHz; transfer rates of 400MBps are achievable for reads and writes.

The EMxxLX is designed with the JEDEC xSPI interface and is compatible serial NOR Flash Quad and Octal SPI memories. With capacity ranging from 4Mb to 128Mb, FPGA system designers now have a multi-purpose, persistent memory that can be used as:

- Configuration Memory to store FPGA bit stream
- Code memory to store CPU instructions with very fast read
- Data memory for the FPGA/SoC with very fast read and writes

A Unified Memory that enables very fast Over-The-Air updates



* With 200MHz DTR serial write

A Versatile Memory For FPGA System on a Chip Designs

- ***Configure as RAM byte addressing or NOR paging***
- ***Supports XIP fast code execution***
- ***No Erase needed- simplify software***
- ***Persistence with no endurance management needed***

FPGA Development Board Support

- **TRENZ CRUVI with EMxxLX Daughter Card**
- **MAS EDB BOARD CERTUS PRO NX PCIe**
- **Visit www.everspin.com for more info**

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