

### **24-BALL BGA PACKAGES**



24-ball BGA 6x8 mm

- Compliant with RoHS, REACH regulations and practices
- Contains no Red Phosphorus
- Lead free
- Standard reflow profile Compatible with similar low-power

SRAM products and other nonvolatile RAM products



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# COMPLIANCE WITH ENVIRONMENTAL REGULATIONS AND DIRECTIVES

**Table 1 – Environmental Regulation and Directive Compliance** 

Environment	Statement Summary	Download Full Statement
ISO9001:2008	Everspin Technologies is in conformance with ISO9001:2008	<u>Certificate</u>
RoHS Directives	Statement of RoHS 1 and the recast Directive 2011/65/EU is commonly referred to as RoHS 2 Compliance. Everspin MRAM products are also "halogen-free".	<u>Full</u> <u>Statement</u>
REACH Regula-	REACH regulations require article suppliers to inform recipi- ents if an article contains a Substance of Very High Concern (SVHC) in excess of 0.1% by weight.  Based on the material content certifications provided by	<u>Full</u>
tions	Everspin's suppliers, none of these substances are present in the materials we use in our products, including packing and shipping materials.	<u>Statement</u>
Red Phosphorus	Everspin Technologies, Inc. MRAM products do not contain Red Phosphorus CAS# 7723-14-0 as an intentional additive.	<u>Full</u> <u>Statement</u>

### **MULTIPLE REFLOW CYCLES AND MOISTURE RESISTANCE**

All Everspin packages are qualified by the procedure defined in IPC/JEDEC joint specification IPC/JEDEC J-STD-020D.1. They are guaranteed to withstand up to three reflow cycles without permanent damage, provided the conditions for the rated moisture resistance level for the part are observed prior to reflow.

Everspin parts are generally rated for MSL Level 3. Exceptions may exist and are noted in their respective data sheet. Please the check the latest individual product data sheet to confirm the rated MSL for the product.



## RECOMMENDED REFLOW TEMPERATURES AND TIMING

Everspin products can be assembled using a standard assembly lead-free reflow profile. The profile below is based on IPC/JEDEC J-STD-020D.1.

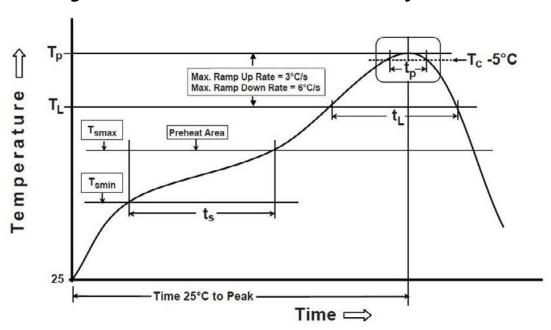


Figure 1 – JEDEC J-STD-020D.1 Assembly Reflow Profile

**Table 2 – Recommended Reflow Times and Temperatures - All Packages** 

Profile Step	Parameter	Symbol	Time/Tem p	Unit
	Temperature minimum	T <sub>SMIN</sub>	150	°C
Preheat / Soak	Temperature maximum	T <sub>SMAX</sub>	200	°C
	Soak Time	tς	60 - 120	Seconds
	Rate from T <sub>L</sub> to T <sub>P</sub>	$T_L$ to $T_P$	3° / Sec Max	° / Sec
Ramp Up	25°C to T <sub>P</sub>		8 minutes max	Minutes
	Liquidous Temperature	$T_L$	217	°C
	Time Above T <sub>L</sub>		60 - 150	Seconds
Reflow	Peak Package Body Tempera- ture	T <sub>P</sub>	260	°C
	Time within 5° of Peak Package Body Temperature		20 - 40	Seconds



	I			
Ramp Down	Rate from T <sub>P</sub> to T <sub>I</sub>	$T_p$ to $T_l$	6° / Sec Max	° / Sec



### THERMAL RESISTANCE

Table 3 - Thermal Resistance MR10Q010 6x8 mm 24-BGA

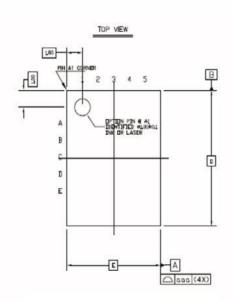
Power (W)	T <sub>A</sub> (°C)	Т <sub>Ј</sub> (°С)		T <sub>J</sub> ⊝ <sub>JA</sub> (°C/W) (°C)		Ψ <sub>JT</sub> (° <b>C/W)</b>	⊝ <sub>JB</sub> (°C/W)
		0 m/s	1 m/s	0 m/s	1 m/s		
0.36	70	94.6	92.8	68.33	63.33	1.67	47.28

Table 4 – Thermal Resistance EM064LX 6x8 mm 24-BGA

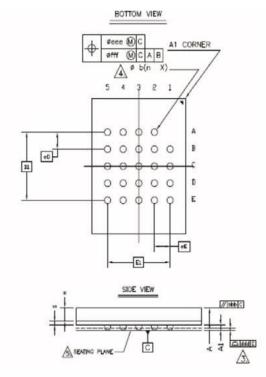
Mode	Power	Package	Die Stack	Ta (C)	⊖ <sub>JA</sub> (C/W)		⊙ <sub>JB</sub> (C/W)	⊝ <sub>JC</sub> (C/W)	Ψ <b>(C/</b>	<b>71</b>
					0 m/s	1 m/s			0 m/s	1 m/s
XSPI	0.5W	BGA	x1	25	65.39	52.84	46.66	14.43	0.72	1.64
XSPI	0.5W	BGA	X2	25	53.07	43.19	34.31	7.95	0.63	2.73

## **PACKAGE OUTLINES BY PRODUCT FAMILY**

<b>Product Family</b>	Density and I/O Width	BGA Package Outline Drawing
MR10Q010	1Mb x8	6x8mm 24-BGA Figure 2 on page 6
EM064LX	64 Mbit xSPI	6x8mm 24-BGA Figure 3 on page 7



Dimension	Min	Nominal	Max
Package Width, E	5.900	6.000	6.100
Package Lenth, D	7.900	8.000	8.100
Package Thickness, A	1.190	1.270	1.350
Solder Ball Stand-Off, A1	0.220		0.320
Solder Ball Width, b	0.320		0.420
Solder Ball Diameter		0.350	
Solder Ball Pitch, eE		1.000	
Solder ball Pitch, eD		1.000	
package Edge Tolerance, aaa		0.100	
Mold Flatness, bbb		0.200	
Solder Ball Coplanarity, ddd		0.080	j
Solder Offset (Package)		0.150	
Solder Offset (Ball)		0.080	
Edge Ball Center to Center, E1		4.000	
Edge Ball Center to Center, D1		4.000	
Ball Count, n		24	

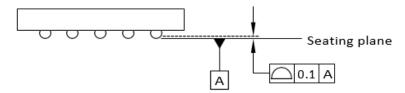


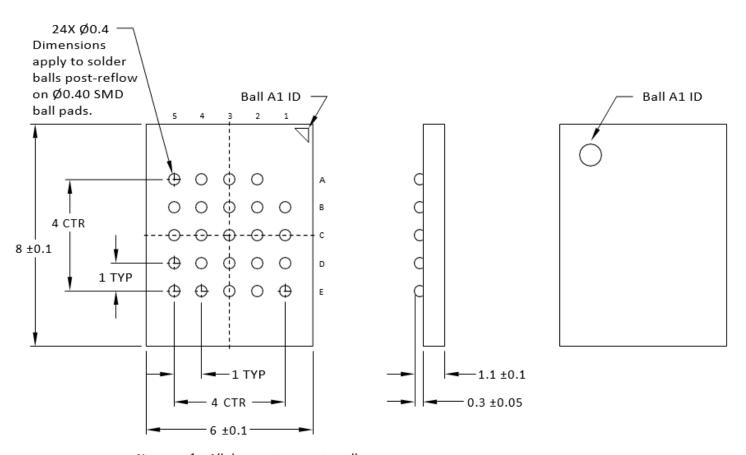
Notes:	
1	Dimensions and tolerances per ASME Y14.5M - 1994.
2	Solder ball position designation per JESD 95-1, SPP-010.
<u>/3</u> \	This dimension includes stand-off height, packge body thickness and lid height, but does not include attached features, e.g. external heatsink or chip capacitors. An intergral heatslug is not considered an attached feature.
4	Dimension is measured at the maximum solder ball diameter, parallel to primary Datum C.
<u>/</u> 5\	Primary Datum C and the seating plane are defined by the spherical crowns of the solder balls.
6	All dimensions are in millimeters.

# **24-BGA PACKAGE OUTLINE DRAWINGS**

Figure 2 – Package Outline 6x8mm 24-ball BGA

#### 24-Ball TBGA, 5 x 5 ball array





Notes: 1. All dimensions are in millimeters.

Figure 3 – Package Outline 6x8 24-ball TBGA



# **REVISION HISTORY**

Revision	Date	Description of Change
1.0	May 21, 2018	Initial release.
1.1	June 28, 2022	Added EM064LX Product Family



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