MTS FPGA Engineer

Everspin Technologies is searching for exceptional candidates to fill an Engineering position in Advanced MRAM development.

Scope of Responsibility/Expectations

In this role, you will support all projects (module products, chip test platforms and customer environments) with a deep understand of how to get the desired results out of the FPGA based platforms. You will work closely with the architects, board design engineers and RTL developers in defining and designing platforms to be sold as high-volume products as well as specialized test and evaluation platforms for Everspin chip products. This position will be in our Austin, TX

Responsibilities:

- Develop process and execute FPGA implementations
- Optimize FPGA implementations for power and performance
- Develop and run experiments to get logic into smaller (and cheaper) FPGAs
- Support both Altera and Xilinx FPGAs

Desired Qualifications:

- Bachelor's degree in Electrical Engineering, Computer Engineering or Computer Science
- Minimum of 3 years of experience
- Excellent understanding of Hardware description languages (VHDL, Verilog) and FPGA design flow
- Strong programming skills across a variety of languages: C, C++, , Perl, Python
- Proficiency with either Xilinx Vivado (preferred) or Altera Quartus. Familiarity with both is a plus.
- Proficient with RTL optimization and placement constraints to obtain timing closure in FPGA
- Understand use of design database management systems (CVS or similar)

The Company

Everspin Technologies is the world's first volume MRAM supplier and continues to lead the industry by advancing the technology and expanding its MRAM product portfolio. The open position is at the company headquarters located in Chandler, Arizona (greater Phoenix metropolitan area). It is a salaried position that includes benefits including: medical, dental, vision plan, paid time off, and a 401k retirement plan.

Contact
Please send resume to jobs@everspin.com