MR256A08B – 32K x 8 MRAM

VERILOG Model Readme File

Introduction

This is the VERILOG model for MR256A08B – a 32K x 8 MRAM Product from Everspin.

Device Summary

The **MR256A08B** is a 262,144-bit magnetoresistive random access memory (MRAM) device organized as 32,768 words of 16 bits. This device offers SRAM Compatible 35ns read/write operation and every data bit written into the memory is automatically protected in the MRAM array. Data retention of greater than 20 years is guaranteed. This device is offered in a 44 Pin TSOP II package and a 48 0.75mm Pitch BGA package.

Model Release Notes

Product Datasheet: http://www.everspin.com/PDF/EST_MR256A08B_prod.pdf
Model Revision: 1.0
Model Release Data: September 2010
Files

1. Readme_MR256A08B - This File
2. MR256A08B.v - Device Model
3. Config_MR256A08B.v - File to Set Timing Parameters

VERILOG Model

MR256A08B.v is the abstracted model of a 32K x 8 MRAM. The model is setup for a 35ns operation.

Warning: These VERILOG models are provided "as is" without warranty of any kind, including, but not limited to, any implied warranty of merchantability and fitness for a particular purpose.

Revision History

Date	Revision	Changes
9/26/2010	1.0	New Model – Initial Release

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